

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	449	chip\$4 same antenna same label\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 06:36
L2	115	1 same (glu\$4 adhesi\$4 tap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 06:58
L5	122	1 same (glu\$4 adhesi\$4 adher\$4 tap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 07:49
L6	19624	(double two) adj (sided faced) adj (glu\$4 adhesi\$4 tap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 07:51
L7	2	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 07:02
L9	50691	(outer outward) near5 (adhesi\$4 adher\$4 glu\$4 tap\$4) near5 surfac\$4	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/22 07:01
L14	675	(label tag) same (glu\$4 adhesi\$4 adher\$4 tap\$4) same ((non adj planar) rough uneven)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:04
L15	7	6 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 07:51

L18	70	6 same (glu\$4 adhesi\$4 adher\$4 tap\$4) same (((non adj planar) rough uneven) near5 surface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 07:59
L19	24	18 and @ad<="19981023"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:09
L20	1	6 and (label tag) same (((non adj planar) rough uneven) near5 surface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:04
L22	206	(label tag) same (glu\$4 adhesi\$4 adher\$4 tap\$4) same (((non adj planar) rough uneven) near5 surface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:04
L23	2	5 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:06
L24	107	22 and @ad<="19981023"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 08:09
S7	201	self near10 (glue\$1 gluing adhesi\$4) near10 circuit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:19
S8	752	(adhesi\$4 glue\$1 gluing) near10 (opening cutout window) near10 circuit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:21

S9	13	S7 and chip and antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:21
S10	6	S8 and chip and antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:20
S11	1282	(adhesi\$4 glue\$1 gluing) near20 (opening cutout window) near20 circuit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:21
S12	12	S11 and chip and antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:53
S13	6	S12 not S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 15:21
S14	458	(double two) adj (sided faced) adj (glu\$4 adhesi\$4 tap\$4) near20 circuit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 06:59
S16	20	S14 and chip and antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 16:03
S18	21	S14 and chip near20 (opening cutout window recess)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 16:13

S19	19624	(double two) adj (sided faced) adj (glu\$4 adhesi\$4 tap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 06:36
S21	28	S19 same (chip ic) near20 (opening cutout window recess)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 17:21
S23	728	S19 same label	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 06:35
S24	4	S23 and chip and antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 17:22
S25	26	S23 and chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/21 17:22